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*James B. Y. Tsui*

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A comprehensive design guide for your digital processing work with today's complex receiver systems. Brand new material brings the reader up-to-date with the latest information on wideband electronic warfare receivers, the ADC testing procedure, frequency channelization and decoding schemes, and the operation of monobit receivers. The book explains how to effectively evaluate ADCs, offers insight on building electronic warfare receivers, and describes zero crossing techniques that are critical to new receiver design. From fundamental concepts and procedures to recent technology advances in digital receivers, it offers practical solutions to all demanding wideband receiver problems. This hands-on reference is packed with 1,103 equations and 315 illustrations that support key topics covered throughout the book.



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